



Development of high-precision distributed wireless microseismic acquisition stations

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Abstract. In recent years, owing to the shortage of oil and gas resources and increased difficulty in mining, traditional (wired) microseismic monitoring equipment has been unable to meet the needs of energy exploitation. Therefore, it is necessary to develop new high-precision seismic exploration and data acquisition systems. In this study, we combined advance acquisition systems with wireless technology to develop a new wireless microseismic acquisition system. The hardware circuit of the acquisition system mainly included a data acquisition board and a main control board. High-precision analog-to-digital conversion and digital filtering technologies were used to provide data with high signal-to-noise ratios, resolution, and fidelity to the acquisition stations. Key technologies were integrated into the ARM of the main control board. Reliable GPS technology was employed to realize synchronous acquisitions among various acquisition stations, and WIFI technology was used to achieve wireless data communication between acquisition stations and the central station, thus improving the data transmission speed and accuracy. After conducting a series of evaluation tests, it was found that the system was stable, convenient to use, and had high data accuracy, therefore providing significant support for the solution to problems encountered in current oil and gas exploration processes, such as the complicated environment and inconvenient construction.

Keywords: Distributed, microseismic, high-precision, analog-to-digital conversion, synchronous acquisition

1 Introduction

From a global view point 50% dependence on imported petroleum can be regarded as a safety warning. However, in 2017, China's foreign dependence exceeded 67.4%, which poses a threat to the energy and economic security of the country ^{[1][2]}. Therefore, the exploration of domestic oil and gas resources in China should be enhanced to contribute to the stability and sustainable development of the country, and also to build a more prosperous society^[3]. With the development of oil and gas exploration technologies, China has entered the difficult stage of oil and gas exploration, namely the exploration of the Pre-Cenozoic marine residual basin ^[4]. This involves the search for oil and gas resources in complex geological areas, such as buried hills and their internal structures, and as well as buildings on high and steep hillsides. In addition to the increase in depth of detection, surface and underground conditions will become more complex. Consequently, there will be new requirements and challenges for seismographs, therefore resulting in an urgent need to develop a novel high-accuracy seismic



data acquisition system suitable for China's conditions^[5]. In recent years, as the country has placed more emphasis on the research and development of geophysical instruments, the number of research units engaged in studies on seismic data acquisition and record systems has continuously increased.

5 The most powerful and widely used geophysical method for oil and gas exploration is reflection seismology^[6]. As the main equipment for seismic exploration, the acquisition station is the core of resource exploration and energy acquisition, with the collection of raw data with high signal-to-noise ratio (SNR), resolution and fidelity being the most crucial aspect^{[1][7]}. A distributed wireless microseismic acquisition station pre-processes and amplifies the analog signals collected by the detector and converts them into digital signals with strong anti-interference ability. The development of seismic instruments has relied heavily on the continuous development of seismic data acquisition methods. Since the advent of digital computers over half a century ago, people have increasingly emphasized the use of numbers to accurately quantify various physical quantities and to transfer information. As a result, digital technologies have been developed in many fields, thus driving the rapid development of microelectronics and computer technologies. Digitization has been proposed in geophysical exploration since the 1960s, with digital seismographs being one of the first examples^[8]. A renowned electronic device manufacturer in the USA, Texas Instruments (TI), developed the DFSIV and DFSV digital seismographs. The French manufacturer SERCEL also introduced the SN328 and SN338 digital seismographs, which used seven-stage instantaneous floating-point amplifiers^[9].

The design concept of the centralized instruments DFSV and SN338 limited the increase of channels, and the travelling distances of the seismic wave analog signals sensed by the detectors along the bulky common depth point (CDP) cables were also too long, exceeding 3 km in some cases. In addition, the distance of signal transmission along the cable varied among different channels, and the signals were severely adulterated by induction^[9]. The SN388, 408UL, 428XL and 508XT systems subsequently introduced by SERCEL offered enhanced processing power, integration level and stability, but still had shortcomings in terms of gain, cost, and human-machine interaction^[10-12].

With the dramatic increase in the number of seismic acquisition channels and exploration in remote regions, the production efficiency of cable systems has been reduced. In regions that are difficult to access and where the environment is vulnerable, cable systems should be supplemented or replaced by wireless systems^[13]. At present, the goal of seismograph research and development is to establish high-resolution, high-precision, synchronous data acquisition, as well as real-time, high-speed and low-power data transmission^[14]. Therefore, the development of a new wireless microseismic digital acquisition system on the basis of traditional seismic instruments is of great significance.

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2 Hardware circuit



2.1 Acquisition circuit design

The acquisition board is the front-end circuit of the main control board and is responsible for processing the analog electrical signals collected by detectors (or electrodes) and converting them into digital signals to be processed. Figure 1 shows the structural block diagram of the simulation board of the acquisition station.

- 5 The front end of the three channels of the ADS1274 analog-to-digital converter (ADC) is connected to a three-component seismometer, and can simultaneously acquire the X, Y, and Z components of the seismic wave. Three analog signals (the seismic wave signal in the X, Y, and Z directions, respectively) input by the detector are first filtered and denoised by the front-end conditioning circuit. Next, the signals are amplified by the programmable amplifier and further filtered by a single-ended differential circuit. They are then converted into a differential signal, input into the ADS1274 for analog-to-digital (AD) conversion, and finally passed to the field programmable gate array (FPGA) circuit for digital filtering.
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The front end of the other channel of the ADS1274 is connected to an electrode (or magnetic bar). The analog signal input by the electrode is amplified by a preamplifier circuit and passes through a filter to eliminate unwanted signals in order to improve the SNR. The filtered analog input signal is further amplified by the main amplifier circuit, which is controlled by the FPGA, and passes through the ADS1274 to be converted into a digital signal. The digital signal is then fed to the FPGA for digitalization.

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Figure 2 shows the operational block diagram of the single channel of the acquisition board design. The AD conversion chip used in this design is the 24-bit ADS1274 manufactured by TI. The chip has four acquisition channels that can acquire data simultaneously. The first three channels are used to collect seismic signals in three directions (X, Y, and Z components, respectively), while the fourth channel is used to acquire electrical signals. The processing steps are similar for the four analog channels.

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Figure 3 is the schematic diagram of the single channel of the acquisition board. The signal first enters the acquisition board through the input protection and impedance matching circuit. The front-end conditioning circuit pre-processes the analog signal to reduce waveform distortion, and the pre-processed signal is filtered using a low-pass filter to remove the high-frequency clutter signals brought in from the detector. The filtered signal is then subsequently amplified. In this design, a programmable gain amplifier AD8253 is used. Through the IO port, the CPU can control its gain to 0 dB, 20 dB, 40 dB, and 60 dB so that the gain can be controlled according to the input signal size in order to achieve a higher resolution of the acquisition board. The amplified signal passes through the AD driver chip so that the single-ended signal is converted into a differential signal (that is, the input into the ADS1274 is a differential signal). The differential signal is then input into the ADS1274 for AD conversion, and the digital signal is output by the AD chip.

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The calibration signal in the schematic diagram is generated by an analog switch chip. The IO port of the FPGA can control the switching frequency of the chip to generate square waves with different frequencies, which can be used to perform self-testing on the acquisition system. The reference voltage module REF5025 generates a 2.5 V standard reference voltage for the AD chip. The clock of the AD chip (CLK) is generated from the FPGA module on the main control board.

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2.2 Acquisition station master control circuit design

The embedded control software in the seismic data acquisition system runs on the main control circuit, which is the core of the distributed wireless microseismic acquisition station. The OMAP3730 processor and the Linux 2.6.37 operating system are adopted in the core control processor. OMAP3730 is a dual-core processor integrating ARM and DSP processors, and can perform signal acquisition control and real-time data storage. The embedded operating system Linux 2.6.37 runs on the processor and is responsible for task scheduling. A high-speed memory management controller chip is used as the data storage medium, while power up management, power supply management, LED driving and keypad management are performed through the general-purpose input/output (GPIO) port of the OMAP3730 processor. The FPGA section is used to accomplish multiple tasks that cannot be performed by general-purpose processors such as: 1. Control of scheduling and execution of all time-related tasks, including the generation of the sampling and real-time clocks, scheduling of related strategies, etc.; 2. Acquisition and control of voltage, current, temperature, and other sensor data; 3. Acquisition system control and data buffering; 4. Wired data communication; 5. Decoding and time synchronization of GPS information.

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The main control board is the core control processing unit of the acquisition station. The design combines the high-performance industrial-grade control system board OMAP3730 and the XC3S2000 FPGA to achieve the following functions:

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(1) System monitoring function: monitoring of power supply voltage, electric current, system temperature, and other boards.

(2) Communication management: USB and serial communications.

(3) Data storage function: Storage in SDHC card.

(4) Acquisition system control and configuration, and data processing functions.

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(5) Time scheduling management functions: synchronization of real-time clock and GPS time, with FPGA being responsible for time-based scheduling control management.

(6) Control of human-computer interaction board.

(7) Storage status indication function.

(8) Communication status indication function.

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(9) Data preprocessing function.

(10) Other auxiliary functions.

The OMAP3730 core control system board circuit integrates the general-purpose memory controller (GPMC) bus, SD card, serial communication, USB, GPIO, and other functional interfaces to simplify the system design. As one USB interface



integrated in the OMAP3730 core control system cannot meet the system implementation requirements, we decided to expand the USB interface to communicate with the WIFI module. A high-speed USB 2.0 PHY chip, USB 3320, was used, and the industrial-grade SDIO wireless module RS9110 was used for the WIFI module. RS9110 is a WIFI module that supports 802.11n b/g and has high-performance wireless transmission capabilities. The clock module provides the acquisition system with accurate clock frequencies, GPS and, other information. The clock module communicates with the master-control FPGA through UART and SPI. The master-control FPGA reads the GPS status information through UART and calculates and adjusts the digital-to-analog output voltage through the continuous count of pulses per second (PPS), so that the frequency of the constant-temperature crystal oscillator is more accurate.

10 3 Overall design of the software

3.1 Summary of software requirement

The software designed in this study adopts the traditional software development life cycle method, and also the top-down and stepwise refinement structural software design approach.

The main functions to be achieved by the software are as follows:

- 15 Disk management, equipment calibration, CRC data verification, data acquisition and storage, real-time data transmission over the network, peripheral acquisition equipment management, GPS information analysis, keypad management, LED status management, wireless network configuration, and large-line network management.

3.2 Software architecture of the distributed wireless microseismic acquisition station

- 20 The embedded control software runs on the Linux2.6.37 system platform. The primary library of the system provides a basic platform for device hardware control functions. Regardless of whether the users are in threads or processes, any user can directly call the system-related interface functions to perform the corresponding tasks. The primary library mainly includes the following: time management interfaces, device management interfaces, disk management interfaces, wireless network management interfaces, network management interfaces, upgrade interfaces, and serial port management interfaces. System calls are an important part of the system and depend on the underlying operating system and the GNU C Library.

The core business layer consists of five sub-modules and a main task, with each component being a separate thread:

- (1) Main thread: responsible for the data acquisition, instrument calibration, and data deletion processes;
- (2) System status thread: responsible for monitoring the system status, including disk capacity, voltage, current, temperature, synchronization status, etc., and handling of response status;
- 30 (3) GPS decoding thread: responsible for decoding GPS information;
- (4) Network communication thread: includes communication with notebooks through wireless network management and communication with large-line transmission systems through large-line network management;



(5) Key event management thread: receives the user's key operations and takes corresponding action according to the operations;

(6) LED lamp management thread: indicates the current status of the system through different statuses of the LED lamp;

The core communication interface module is an interface for communication between different threads to establish a message communication mechanism among threads.

Under the core business layer, the system management module for the electromagnetic data acquisition system mainly includes a time management module, a device driver module, a disk management module, wireless network configuration, and serial port configuration.

4 Wireless microseismic acquisition station communication

The field work mode of the distributed wireless acquisition system is shown in Figure 6. A relay access point (AP) is placed in a certain area as a relay point to connect with a certain number of wireless seismic acquisition stations for wireless data transmission. Multiple relay APs are used to cover all wireless acquisition stations in the construction area. The central station AP connects with each relay AP through the omnidirectional coverage antenna to issue commands and collect real-time data.

The communication and interaction process of the wireless network is shown in Figure 7.

5 Acquisition station performance test

5.1 Noise test

The equivalent input noise refers to the output of the seismic acquisition station when the input end of the station is connected to the standard resistance. The measurement result is converted into the root mean square (RMS) voltage value, usually expressed in microvolts. The equivalent input noise is mainly composed of ADC quantization noise and the thermal noise of the components and is directly superimposed on the input seismic signal during operation. Therefore, it determines the capability of the instrument to distinguish weak signals, wherein lower equivalent input noise is more desirable.

When the equivalent input noise is tested, the analog signal input terminal must be connected with a terminal test resistor. In the present design, a resistor of 1 k Ω resistance was used. During the test, the sampling rate of the ADS1274 was set to 1000 SPS, and data corresponding to preamp gains of $\times 1$, $\times 10$, $\times 100$, and $\times 1000$ were respectively collected. Data from 8192 sample points were collected for processing and analysis.

The calculation method in the equivalent input noise test is as follows: First, the average value of amplitude X_i for N samples was calculated using Eq. (5-1) to obtain the DC offset (dc) of the noise signal.



$$dc = \frac{1}{N} \sum_{i=0}^N X_i \quad (5-1)$$

Next, the RMS after drift removal, which is the equivalent input noise of the instrument, was then calculated as.

$$RMS = \sqrt{\frac{1}{N} \sum_{i=0}^N (X_i - dc)^2} \quad (5-2)$$

Then, by analyzing the data of 8192 sampling points, the equivalent input noise waveform was plotted, as shown in Figure 8. Table 1 presents the equivalent noise output results of the preprogrammed amplifier with different gains. According to the obtained results from the processed data, it can be seen that when the amplification of the programmable amplifier was 40 dB, the equivalent error of the acquisition station was 0.51 μV (approximately 0.5 μV). Therefore, it can be concluded that the acquisition accuracy of the acquisition station satisfied the expected requirements.

5.2 Harmonic distortion test

Harmonic distortion refers to the nonlinear distortion of the output of a seismic instrument when a sinusoidal signal is inputted. It is usually measured by the ratio of the amplitude of each harmonic component to the RMS of the fundamental signal amplitude. The harmonic distortion of the seismograph reflects the fidelity of the seismic instrument in the acquisition and recording of seismic signals. This indicator is closely related to the noise and instantaneous dynamic range of the instrument. If the harmonic distortion of the instrument is excessively large, each harmonic component of the seismic signal will be converted into noise, thus reducing the dynamic range of the system. At the same time, if the harmonic distortion of the instrument is excessively large, the high-order harmonics generated by the low-frequency surface waves with strong amplitude are likely to be mixed into the frequency band of the low-amplitude effective signal, thereby causing interference. The harmonic distortion of seismic instruments is therefore an important performance indicator.

During the test, a signal generator was used to output a sinusoidal signal of 31.25 Hz, with a peak-to-peak value of 1 V, preamp gain of $\times 1$, and sampling rate of 1000 SPS. By analyzing the data collected from 8192 sampling points, the waveform was generated in Matlab, as shown in Figure 9.

Discrete Fourier transform (DFT) of length N was performed on the signal sequence $x(n)$ to obtain the discrete frequency spectrum $X(k)$, and to find the maximum spectral magnitude point $|X(p)|$. The corresponding discrete frequency point p in the spectrum diagram is the corresponding frequency point of the fundamental wave. Assuming that each harmonic amplitude is A_i , the harmonic distortion (THD) was computed using Eq. (5-3), and the fifth harmonic was obtained in the actual calculation.



$$THD = 20 \times \lg \left(\sqrt{\frac{A_2^2 + A_3^2 + A_4^2 + \dots + A_N^2}{A_1^2}} \right) \quad (5-3)$$

When the data length is a non-integer multiple of the period of the sine wave, the energy of the fundamental wave and the harmonics wave will leak into the adjacent frequency band, thus resulting in spectral leakage. In the actual calculation of harmonic distortion, direct use of DFT for spectral analysis often causes frequency spectrum leakage, so that the calculated spectrum does not accurately reflect the frequency characteristics of the signal. Therefore, it is usually necessary to perform window processing on the measured signal, with a minimum energy window being most optimum.

The test result is shown in Figure 10.

5.3 Synchronization test of acquisition stations

In seismic exploration experiments, the precise collection of seismic wave signals and the recording of their corresponding travel time are extremely important. This requires data to be acquired synchronously by each sensor ^[15].

In the present design, GPS technology is the key to accomplishing synchronous acquisition. The LEA-6T timing receiver, which can output PPS signals with a precision of up to 15 ns, has been adopted in this design. At the same time, it has dedicated frequency and PPS output pins, with a precision as high as 10^{-12} .

During the test, acquisition was conducted on 32 acquisition stations simultaneously. Figure 11 shows the acquisition of 32 stations under the same seismic source. As seen from the figure, when vibrations were emitted from the source, the entire 32 acquisition stations collected data almost simultaneously, which indicates good synchronization performance between them.

5.4 Acquisition station performance comparison

After the performance test of the acquisition station was completed, the wireless microseismic acquisition station was compared with a new type of seismic acquisition station on the market. The selected acquisition station was the intelligent acquisition station displayed by the Italian DTCC/ SmartSolo® at CPS/ SEG 2018 in Beijing, April 24-27. Comparing the six aspects of equivalent input noise, timing accuracy, data transmission, etc. Which reflects the superiority of high-precision distributed wireless microseismic acquisition stations in some aspects. Table 2 shows the specific performance comparison results.

6. Conclusion

In this study, a high-precision distributed wireless microseismic acquisition system has been designed for oil and gas exploration. The system design, which was based on the ADS1274 chip manufactured by TI, made full use of the four channels



of the chip to collect vibration signals in three directions and one electrical signal, respectively. Furthermore, the acquisition system used GPS and WIFI technologies to achieve distributed wireless acquisition. After single-station testing and multi-station joint testing of the acquisition stations, the results show that the system's performance in equivalent noise, harmonic distortion testing, and synchronous acquisition was able to meet the expected requirements. As such, the acquisition system is currently being practically applied in production operations.

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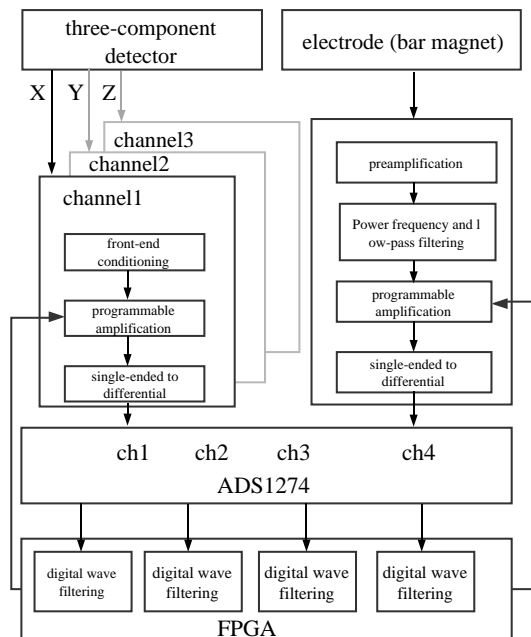
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 10 acquisition station based on system-on-a-programmable-chip technology[J]. Annals of Geophysics, 2013, 56(3):1-8.

[15] Teng, JW. Ponder and research on the genesis and occurrence of strong earthquakes and the prediction of their place, time and intensity[J]. Chinese Journal Of Geophysics-Chinese Edition, 2010, 53(8):1749-1766.



15 **Figure 1: Structural block diagram of the hybrid acquisition system based on the ADS1274 high-precision seismic electrical method.**

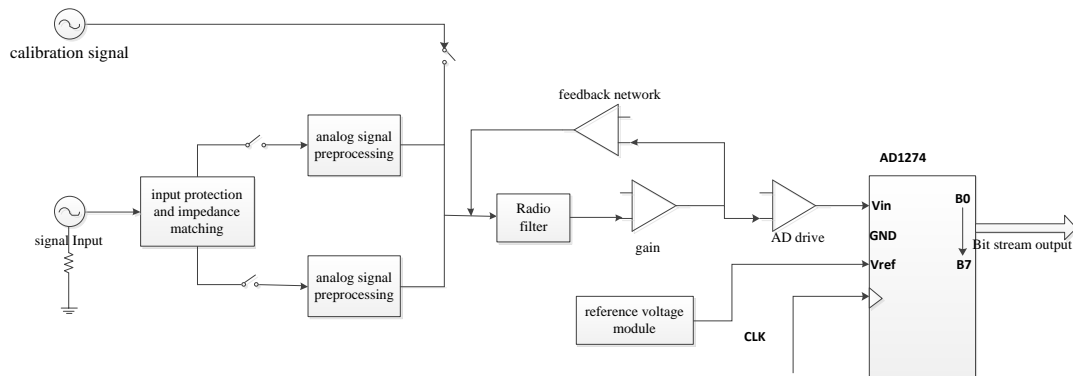
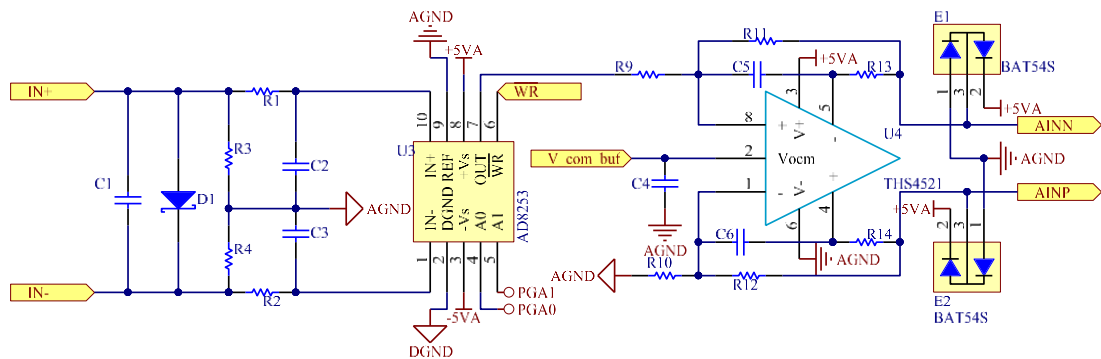


Figure 2: Operational block diagram of the single channel of the acquisition board.



5 Figure 3: Schematic of the single channel of the acquisition board.

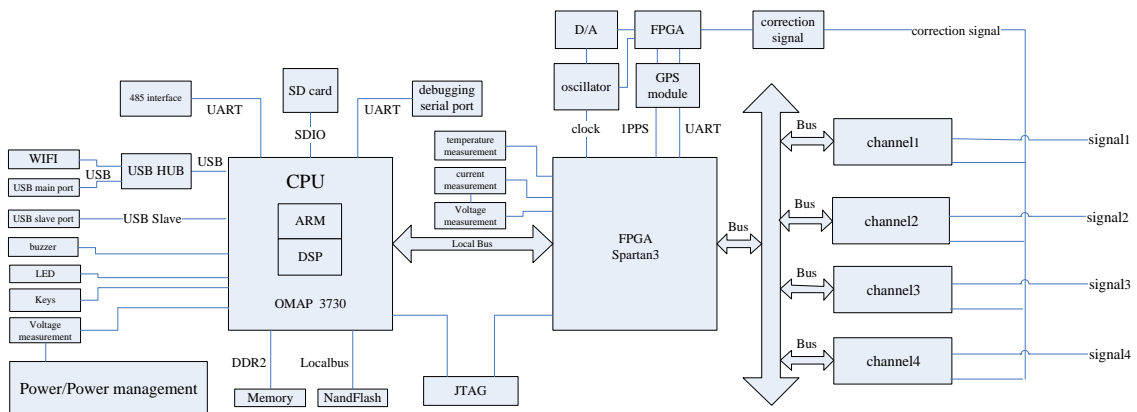


Figure 4: Block diagram of the hardware design for the main control circuit of the wireless microseismic acquisition station.

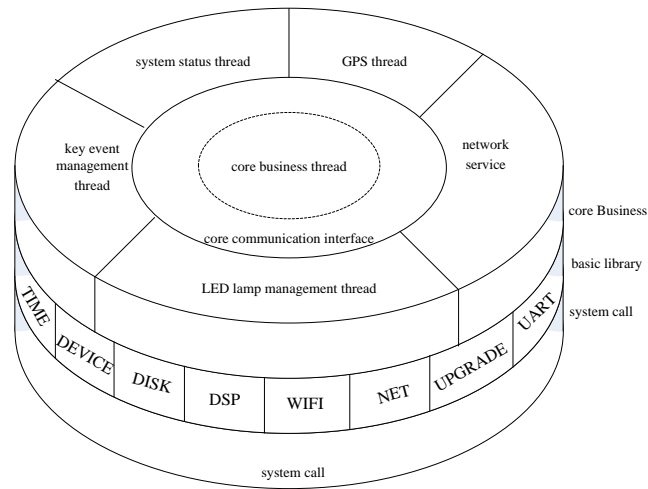


Figure 5: Overall software architecture diagram.

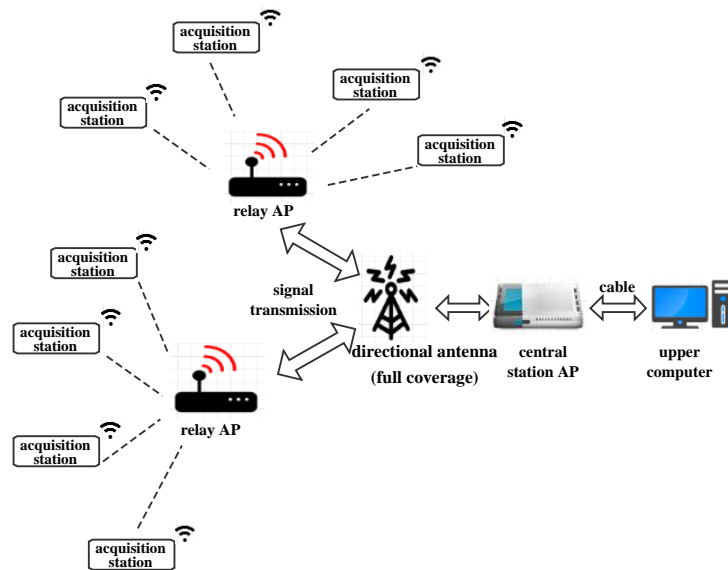


Figure 6: Schematic of a distributed wireless acquisition system.

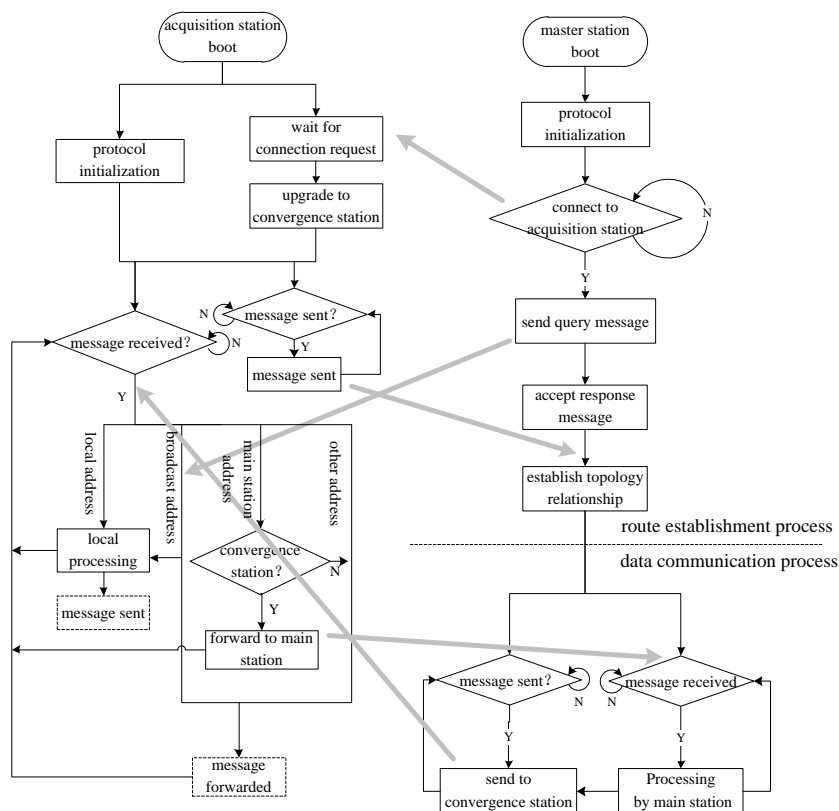


Figure 8: Equivalent input noise waveform of the acquisition board.

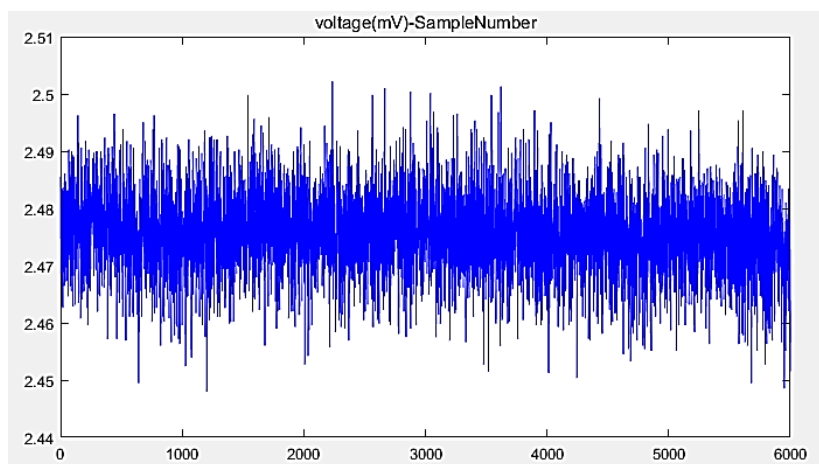


Figure 8: Equivalent input noise waveform of the acquisition board.

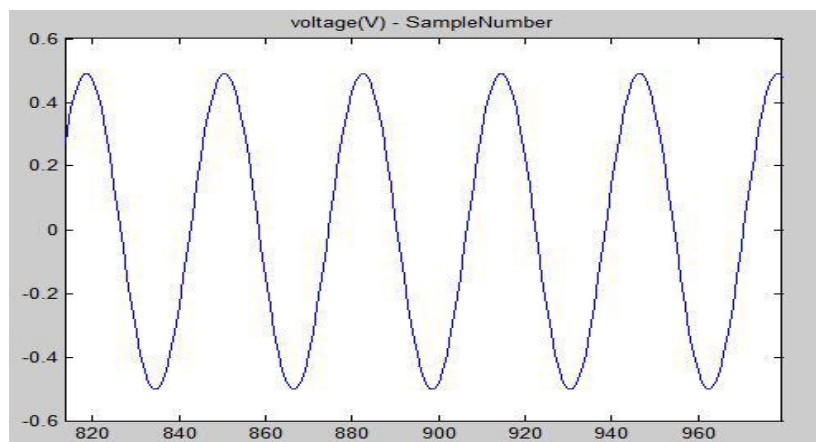


Figure 9: Generated sinusoidal signal waveform.

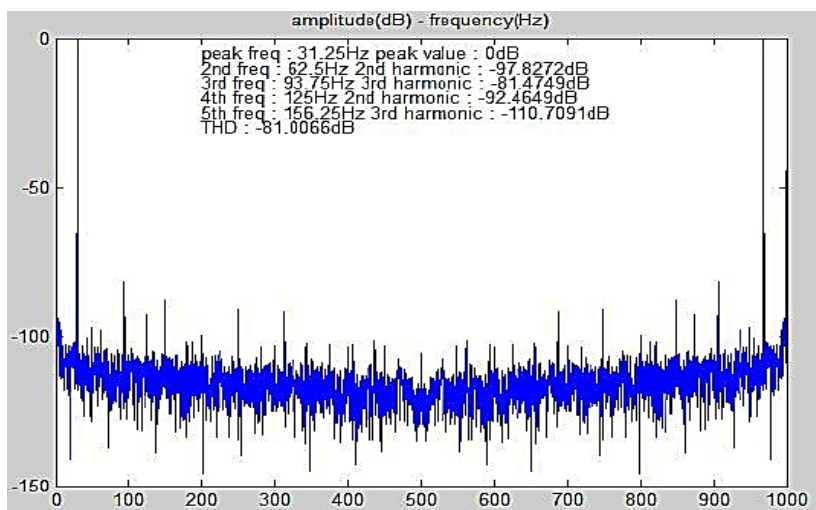


Figure 10: Harmonic distortion test chart.

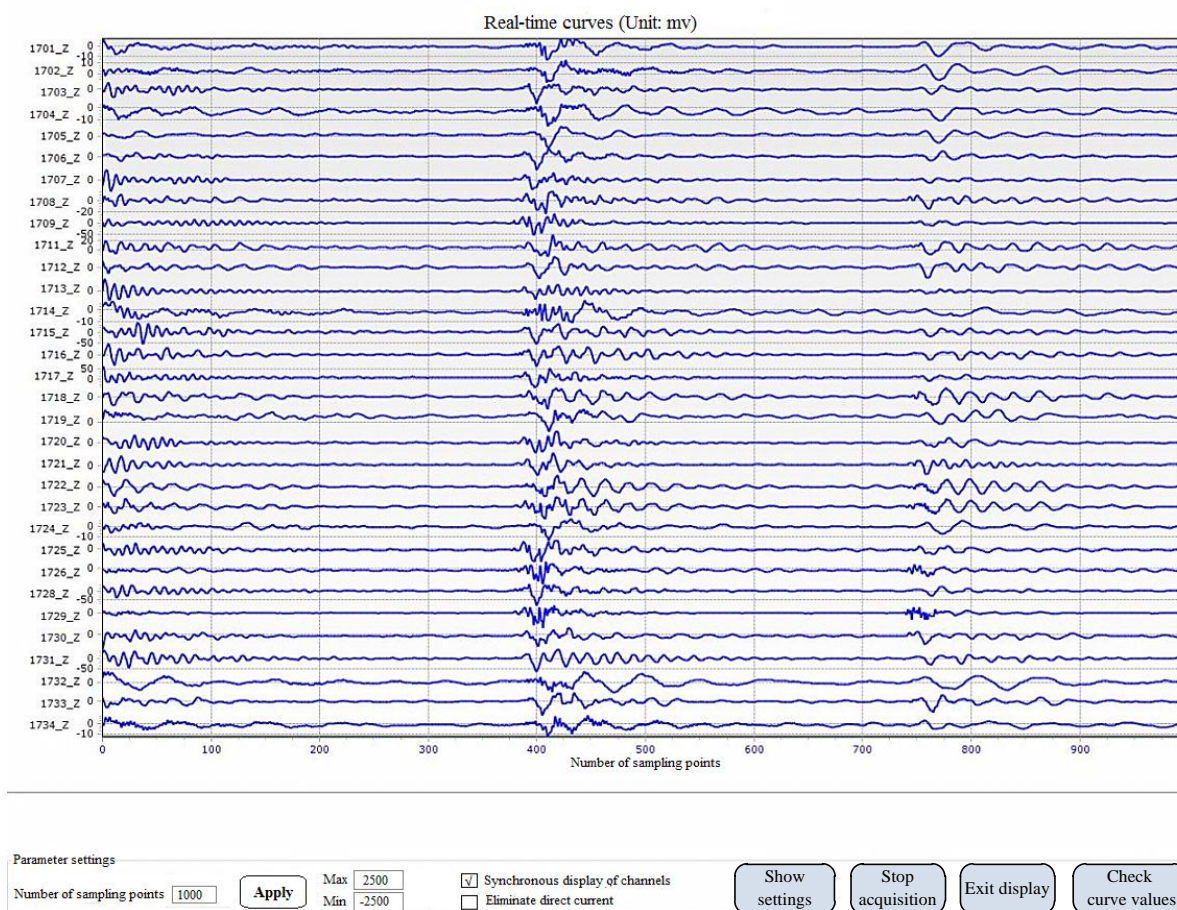


Figure 11: Test chart of the acquisition station synchronization.

Table 1: Equivalent input noise test results.

Preamplifier Gain	Equivalent Input Noise (μV)
$\times 1$	6.06
$\times 10$	0.91
$\times 100$	0.51
$\times 1000$	0.30



Table 2: Acquisition station performance index comparison results.

Indicator	High-precision distributed wireless microseismic acquisition stations	SMARTSOLO
ADC resolution	24bits	24bits
Equivalent Input Noise	0.91 μ V@20dB(RMS)	1.23 μ V@12dB(RMS)
Channels	4(Three seismic channels, one electrical channel)	1(Only one seismic channel)
Data Transmission	Storage in SD card + wireless transmission	Storage in SD card
Maximum Input Signal	2.5 peak@0dB	2.5 peak@0dB
Timing Accuracy	<100ns, GPS disciplined	<20 μ s, GPS disciplined